



Derris H. Banks

Derris H. Banks, now Director for Technology Center 2600, joined the United States Patent and Trademark Office (USPTO) in June of 1994 examining applications concerning semiconductor fabrication and abrasive tool manufacturing found in Technology Center 3700. He was promoted to SPE in December 2000 and managed various Art Units that examined metal working and shaping, toys, printed matter, wood working, solid material comminution or disintegration, and presses. He has served on numerous special projects such as Detailee to the Office of the Commissioner, Assistant to the Acting Director of Office of Patent Training, Class Manager in the Office of Patent Training, Nationwide Workforce Team member, and Cooperative Patent Classification (CPC) Quality Assurance and Outreach Team member. He received a Bachelor of Science degree in mechanical engineering from the University of Maryland at College Park in 1993, and his Master of Public Administration degree from the Keller Graduate School of Management at DeVry University in 2007.

Emily Le

Emily Le is a Supervisory Patent Examiner in Technology Center (TC) 1700, currently working as a Cooperative Patent Classification (CPC) Project Coordinator. Emily received her Bachelor of Science degree in Chemistry from Virginia Tech and joined the United States Patent and Trademark Office (USPTO) as a Patent Examiner in TC 1600 in 2003.

Seema S. Rao

Seema S. Rao, Technology Center 2100 Director, joined the United States Patent and Trademark Office (USPTO) in November of 1994 examining applications in the multiplex communication technology area. She became a Supervisory Patent Examiner (SPE) in 2002 and managed various Art Units that examined multiplex communication and related technology areas. She received a Bachelor of Science degree in Electrical and Electronics engineering from India and Master of Science in Telecommunications System Management from the University of Maryland at College Park in 1994.

Paul L. Rodriguez

Paul L. Rodriguez, Supervisor Review Quality Assurance Specialist in the Office of Patent Quality Assurance. Joined the United States Patent and Trademark Office (USPTO) in September 1999 examining applications in the Computer Control System area. He became a Supervisory Patent Examiner (SPE) in 2006 and managed Art Units which examined Computer Modeling, Simulation and Emulation applications. He received a Bachelor of Science degree in Electrical Engineering Technology from Old Dominion University in 1999. Prior to joining the USPTO, worked as a Sr. Engineer supporting US Navy and Foreign Military Shipbuilding Programs.